

Name: Key

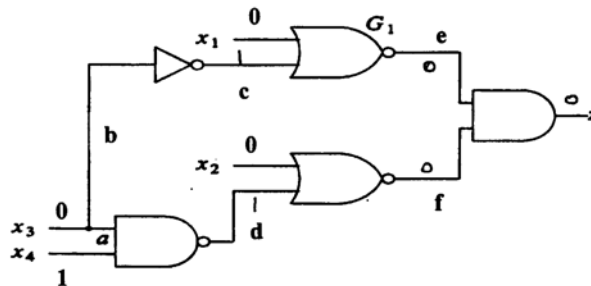
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COE 464, Term 042
Testing of Digital Circuits

Quiz# 3

Date: Tuesday, March 22, 2005

Q1. Consider the circuit given below and assume that you have all the single stuck-at faults in the circuit, without fault collapsing, to be fault simulated:



i. Using Deductive fault simulation technique, determine the set of faults detected by the test vector $x_1x_2x_3x_4 = \{0001\}$.

$$\begin{aligned}
 L_{x_1} &= \{x_1, 1\}, & L_{x_2} &= \{x_2, 1\}, & L_{x_3} &= \{x_3, 1\}, & L_{x_4} &= \{x_4, 0\} \\
 L_a &= L_{x_3} \cup \{a_1\} = \{x_3, a_1\}, & L_b &= L_{x_3} \cup \{b_1\} = \{x_3, b_1\} \\
 L_c &= L_b \cup \{c_0\} = \{x_3, b_1, c_0\} \\
 L_d &= L_a - L_{x_4} = \{x_3, a_1\} - \{x_4, 0\} = \{x_3, a_1\} \cup \{d_0\} = \{x_3, a_1, d_0\} \\
 L_e &= \{L_c - L_{x_1}\} \cup \{e_1\} = \{\{x_3, b_1, c_0\} - \{x_1, 1\}\} \cup \{e_1\} \\
 &= \{x_3, b_1, c_0, e_1\} \\
 L_f &= \{L_d - L_{x_2}\} \cup \{f_1\} = \{\{x_3, a_1, d_0\} - \{x_2, 1\}\} \cup \{f_1\} \\
 &= \{x_3, a_1, d_0, f_1\} \\
 L_z &= \{L_e \cap L_f\} \cup \{z_1\} = \{x_3, z_1\} \\
 \text{So, the faults detected are } &x_3 \text{ s-a-1 and } z \text{ s-a-1.}
 \end{aligned}$$