

Jan. 7, 2010

COMPUTER ENGINEERING DEPARTMENT

COE 561

Digital System Design and Synthesis

MAJOR EXAM II

(Open Book Exam)

First Semester (091)

Time: 3:30-6:00 PM

Student Name : _KEY_____

Student ID. : _____

Question	Max Points	Score
Q1	30	
Q2	15	
Q3	15	
Q4	20	
Q5	20	
Total	100	

[30 Points]

(Q1) Consider the function $F(A, B, C, D)$ with **ON-SET**= $\Sigma m(5, 6, 7, 13)$ and **OFF-SET**= $\Sigma m(1, 8, 12)$. Note that you do not need to use the positional-cube notation in your solution.

- (i) **Expand** the minterm $ABC'D$ using ESPRESSO heuristics.
- (ii) A cover of the function is given by $F = C + BD$. **Reduce** the cube C using Theorem 7.4.1.
- (iii) Use Corollary 7.4.1 to check if the implicant C is an **essential** prime implicant.

(i) Expand the minterm $ABC'D$

Free set = $\{1, 3, 6, 7\}$

Column 7 can't be raised as it has distance 1 from the offset.

Examining the off-set, we can see that col. 6 is all 0s in the off-set. Thus, col. 6 can always be raised. Thus, free set = $\{1, 3\}$.

The overexpanded cube = D

Thus, we need to check the cubes $\bar{A}B\bar{C}D$ and $\bar{A}BCD$ for being feasibly covered.

$SC(\bar{A}B\bar{C}D, \bar{A}B\bar{C}D) = B\bar{C}D$ feasible

$SC(\bar{A}B\bar{C}D, \bar{A}BCD) = BD$ feasible

Thus, the minterm is expanded to BD as it covers the other feasibly covered cube.

Thus, free set = $\{3\}$. This col. can't be raised as it has distance 1 from the offset.

Thus, the expanded cube is BD .

(ii) Reduce the cube C

$$Q = BD + \underbrace{\overline{A}\overline{C}\overline{D} + \overline{B}C + AC + \overline{A}\overline{B}D}_{DC\text{-set}}$$

$$= \overline{B}\overline{D} [\overline{A}\overline{C} + C + AC] + \overline{B}D [C + AC + A] \\ + \overline{B}\overline{D} [\overline{A}\overline{C} + AC] + BD [1]$$

$$= \overline{B}\overline{D} [\overline{A} + C] + \overline{B}D [A + C] + \overline{B}\overline{D} [\overline{A}\overline{C} + AC] \\ + BD [1]$$

$$\overline{Q} = \overline{B}\overline{D} [A\overline{C}] + \overline{B}D [\overline{A}C] + \overline{B}\overline{D} [A\overline{C} + \overline{A}C]$$

$$\overline{Q}_c = \overline{B}\overline{D}\overline{A}, \quad SC(\overline{Q}_c) = \overline{A}\overline{B}\overline{D}$$

$$C \wedge SC(\overline{Q}_c) = \overline{A}\overline{B}\overline{C}\overline{D}$$

Thus, the cube C is reduced to $\overline{A}\overline{B}\overline{C}\overline{D}$.

(iii) Check if C is an essential prime implicant

$$\alpha = C$$

$$G = \{BD, \overline{A}\overline{C}\overline{D}, \overline{B}C, AC, \overline{A}\overline{B}D\}$$

$$G \# \alpha = \{\overline{B}\overline{C}\overline{D}, \overline{A}\overline{C}\overline{D}, \overline{A}\overline{B}\overline{C}\overline{D}\}$$

$$H = \text{Consensus}(G \# \alpha, \alpha)$$

$$= \{BD, \overline{A}\overline{D}, \overline{A}\overline{B}D\}$$

$$\{H \cup DC\}_\alpha = \{BD, \overline{A}\overline{D}, \overline{A}\overline{B}D, \overline{A}\overline{C}\overline{D}, \overline{B}C, AC, \overline{A}\overline{B}D\}_C$$

$$= \{BD, \overline{A}\overline{D}, \overline{A}\overline{B}D, \overline{B}, A, \overline{A}\overline{B}D\}$$

$$= \{BD, \overline{A}\overline{D}, \overline{B}, A\}$$

$$= \{0, \overline{0}, \overline{B}, A\} \Rightarrow \text{Tautology}$$

Thus, the implicant C is not an essential prime implicant.

[15 Points]

(Q2) Consider the logic network defined by the following expression:

$$x = ace + acf + bce + bcf + de + df$$

Using the recursive procedure **KERNELS**, compute all the kernels and co-kernels of x . Show all the steps of the algorithm. Assume the following lexicographic order: {a, b, c, d, e, f}.

$c=1$ (a):

$$\text{cubes}(x, a) = \{ace, acf\} \geq 2$$

$$C = ac$$

The kernel $e+f$ will be returned

$c=2$ (b):

$$\text{cubes}(x, b) = \{bce, bcf\} \geq 2$$

$$C = bc$$

The kernel $e+f$ will be returned

$c=3$ (c):

$$\text{cubes}(x, c) = \{ace, acf, bce, bcf\}$$

$$C = c$$

Kernel found: $ae + af + be + bf$

Recursive call on the kernel with $c=4$ {d}

- No kernel will be found for literal d

- $c=5$:

$$\text{cubes containing } e: \{ae, be\} \geq 2$$

$$C = e$$

The kernel $a+b$ is generated

- $c=6$:

$$\text{cubes containing } f: \{af, bf\}$$

$$C = f$$

The kernel $a+b$ is generated

$c=4 (d)$:

$$\text{cubes}(x, d) = \{de, df\} \geq 2$$

$$c = d$$

The kernel $e+f$ is generated

 $c=5 (e)$:

$$\text{cubes}(x, e) = \{ace, bce, de\}$$

$$c = e$$

The kernel $ac+bc+d$ is generated
recursive call with $d=6$ does not produce
any kernel

 $c=6 (f)$:

$$\text{cubes}(x, f) = \{acf, bcf, df\}$$

$$c = f$$

The kernel $ac+bc+d$ is generated
recursive call with $c=7$ does not produce
any kernel.

Thus, the list of kernels and co-kernels of x are:

Kernel	co-Kernel
$e+f$	ac, bc, d
$ae+af+be+bf$	c
$a+b$	ce, cf
$ac+bc+d$	e, f
$ace+acf+bce+bcf+de+df$	1

[15 Points]

(Q3) Consider the logic network defined by the following expression:

$$x = abe'f + ab'e + ab'f + a'de'f + a'd'e + a'd'f + ce'f$$

Compute the weight of the double cube divisors $d_1 = ab + a'd$ and $d_2 = e + f$. Extract the double cube divisor with the highest weight and show the resulting network after extraction and the number of literals saved.

Double cube Divisor	Base
$d_1 = ab + a'd$	$e\bar{f}$
$\bar{d}_1 = a\bar{b} + \bar{a}\bar{d}$	e, f
$d_2 = e + f$	$a\bar{b}, \bar{a}\bar{d}$

$$\text{weight}(d_1) = 3 + 4 - 3 - 4 + 2 + 1 + 1 = 9$$

$$\text{weight}(d_2) = 2 + 2 - 2 - 2 + 2 + 2 + 3 = 7$$

Since d_1 has a higher weight, it will be extracted.

The resulting network after extraction of d_1 is:

$$[1] = ab + a'd$$

$$x = [1]e\bar{f} + [\bar{1}]e + [\bar{1}]f + ce\bar{f}$$

14 literals

$$\text{original number of literals} = 23$$

$$\text{Number of literals saved} = 9 \text{ literals}$$

(Q4) Consider the logic network defined by the following expressions with inputs $\{a, b, c, d, e\}$ and output $\{y\}$:

$$x = ab + a'd$$

$$w = x'd' + bc'$$

$$y = (w \oplus e) a'b$$

- (i) Simplify the function w based on the utilization of don't care conditions.
 (ii) Based on perturbation analysis starting with the original network, determine if it is possible to change the implementation of x to $x = a$.

(i) We need to compute SOC_x and ODC_w to simplify w .

$$\begin{aligned} SOC_x &= x \oplus (ab + \bar{a}d) \\ &= x(a\bar{b} + \bar{a}\bar{d}) + \bar{x}(ab + \bar{a}d) \\ &= x\bar{a}\bar{b} + x\bar{a}\bar{d} + \bar{x}ab + \bar{x}\bar{a}d \end{aligned}$$

$$\begin{aligned} ODC_w &= \overline{y_w \oplus y_{\bar{w}}} = \overline{e\bar{a}b \oplus \bar{e}\bar{a}b} \\ &= \overline{\bar{a}b(e \oplus \bar{e})} = \overline{\bar{a}b} = a + \bar{b} \end{aligned}$$

				$x=0$
cd	00	01	11	10
ab	00	01	11	10
	00	01	11	10
	01	11	10	00
	11	10	00	01
	10	00	01	11

				$x=1$
cd	00	01	11	10
ab	00	01	11	10
	00	01	11	10
	01	11	10	00
	11	10	00	01
	10	00	01	11

$$w = \bar{x} + \bar{c}$$

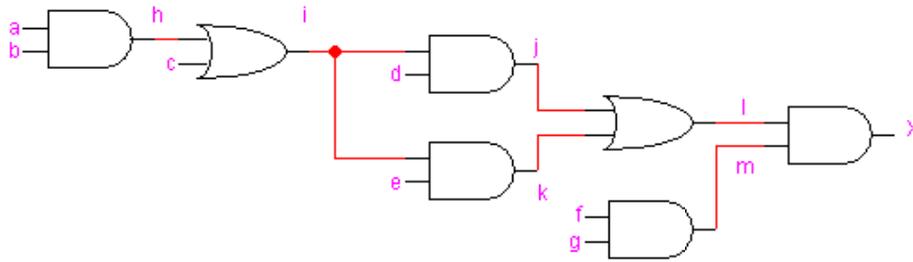
$$\text{or } w = \bar{d} + \bar{c}$$

$$\begin{aligned}
 \text{(ii)} \quad \delta &= (ab + \bar{a}d) \oplus a \\
 &\equiv (a\bar{b} + \bar{a}d)a + (ab + \bar{a}d)\bar{a} \\
 &= a\bar{b} + \bar{a}d
 \end{aligned}$$

$$\text{ODC}_x = d + b\bar{c} + a + \bar{b}$$

Since $\delta \leq \text{ODC}_x$, then it is possible to change the implementation of x to $x = d$

(Q5) Consider the logic network below with inputs $\{a, b, c, d, e, f, g\}$ and output $\{X\}$:



Assume that the delay of a gate is related to the number of its inputs i.e. the delay of a 2-input AND gate is 2. Also, assume that the input data-ready times are zero for all inputs.

- (i) Compute the data ready times and slacks for all vertices in the network.
- (ii) Determine the topological critical path.
- (iii) Suggest an implementation of the function X using only 2-input gates to reduce the delay of the circuit to the minimum possible and determine the maximum propagation delay in the optimized circuit. Has the area been affected?

(i)

	Data ready time	Required time	Slack
	$t_a = 0$	$\bar{t}_a = 0$	$s_a = 0$
	$t_b = 0$	$\bar{t}_b = 0$	$s_b = 0$
	$t_c = 0$	$\bar{t}_c = 2$	$s_c = 2 - 0 = 2$
	$t_d = 0$	$\bar{t}_d = 4$	$s_d = 4 - 0 = 4$
	$t_e = 0$	$\bar{t}_e = 4$	$s_e = 4 - 0 = 4$
	$t_f = 0$	$\bar{t}_f = 6$	$s_f = 6 - 0 = 6$
	$t_g = 0$	$\bar{t}_g = 6$	$s_g = 6 - 0 = 6$
	$t_h = 2$	$\bar{t}_h = 2$	$s_h = 2 - 2 = 0$
	$t_i = 4$	$\bar{t}_i = 4$	$s_i = 4 - 4 = 0$
	$t_j = 6$	$\bar{t}_j = 6$	$s_j = 6 - 6 = 0$
	$t_k = 6$	$\bar{t}_k = 6$	$s_k = 6 - 6 = 0$
	$t_l = 8$	$\bar{t}_l = 8$	$s_l = 8 - 8 = 0$
	$t_m = 2$	$\bar{t}_m = 8$	$s_m = 8 - 2 = 6$
	$t_x = 10$	$\bar{t}_x = 10$	$s_x = 10 - 10 = 0$

(ii) The topological critical paths are:

$$\{a, h, i, j, l, x\}$$

$$\{a, h, i, k, l, x\}$$

$$\{b, h, i, j, l, x\}$$

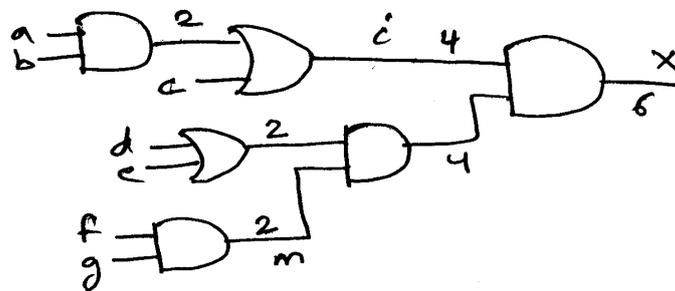
$$\{b, h, i, k, l, x\}$$

(iii) To optimize the delay of the network, we need to improve the delay of nodes in the critical path.

$$d = cd + ce \Rightarrow d = c(d+e)$$

$$X = d \cdot m = c \cdot [(d+e) \cdot m]$$

The resulting network:



The maximum propagation delay in the optimized circuit is 6.

Number of literals in the original circuit = 14 literals

Number of literals in optimized circuit = 12 literals

Thus, the area has also been reduced.