

Name: Key

Id#

COE 360, Principles of VLSI Design, Term 043
Quiz# 3

Date: Tuesday, July 19

Q1. Assuming nMOS enhancement transistor threshold voltage V_{tn} of 1.0 V, depletion transistor threshold voltage V_{td} of -2.0 V, and pMOS enhancement transistor threshold voltage V_{tp} of -1.0 V, write down the voltages of the indicated nodes below. Assume that the body effect is negligible.

