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COE 342, Term 012
Data & Computer Communications

Quiz# 4

Date: Wednesday, May 22, 2002

- Q1. A CRC is constructed to generate a 4-bit FCS for an 8-bit message. The generator polynomial is $X^4 + X^3 + 1$
- i. Draw the linear feedback shift register (LFSR) circuit that would perform this task.
 - ii. Determine the FCS that will be generated for the message 10011011.
 - iii. Suppose that the receiver receives the following message 110010011001. Will the receiver assume the message faulty or fault-free? Justify your answer.