

Name:

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COE 308, Computer Architecture, Term 982
Quiz# 1

Date: Monday, Feb. 15

(I) A Pentium II microprocessor has 64 Mbyte memory. Show the memory organization and the processor-memory interface such that the processor will be able to access two 32-bits simultaneously and possibly from different locations.

(II) Subtract the following numbers assuming both sign-magnitude and two's complement representation. Indicate when an overflow occurs:

(i) 11001100 - 101110

(ii) 01100 - 11101

(III) Describe three main differences between CISC and RISC processors.