

Name: KEY

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COE 205, Term 031
Computer Organization & Assembly Programming

Quiz# 2

Date: Tuesday, Oct. 7, 2003

Q1. Consider an 8-bit register that has the binary number 10001100. Determine the decimal value of the number if it represents:

i. An unsigned number.

$$= 128 + 8 + 4 = 140$$

ii. A signed number in sign-magnitude representation.

$$= - 12$$

iii. A signed number in 1's complement representation.

The 1's complement of 10001100 is 01110011 = 127-12= 115
So, the number 10001100 represents -115

iv. A signed number in 2's complement representation.

The 2's complement of 10001100 is 01110100 = 127-11= 114
So, the number 10001100 represents -114

Q2. Perform the following arithmetic operations assuming that numbers are represented using 8-bit 2's complement representation. Indicate in your answer when an overflow occurs.

i. 1F + 5F

$$\begin{array}{r} 1 \\ 1 \\ + 5 \\ \hline 7 \end{array}$$

There is no overflow. We added two positive numbers and got a positive number.

