

Q3. Which of the following 8086 registers is part of the ISA: **Instruction Pointer (IP), Instruction Register (IR), Memory Address Register (MAR), AX, SP.**

Q4. Determine the machine type and the size of the address bus and the data bus for the 8086 and the Pentium processors.

Q5. Determine whether the following operations are performed in the fetch or execute phase: **Reading an instruction from Memory, Reading Operands from Memory, Decoding an Instruction, Incrementing the Instruction Pointer.**

Q6. Order the following storage devices once in terms of speed and once in terms of capacity: RAM, Registers, Tape, Hard Disk, and Cache.