

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
COLLEGE OF COMPUTER SCIENCES & ENGINEERING

COMPUTER ENGINEERING DEPARTMENT

**COE 205 Computer Organization & Assembly Language
Syllabus - Term 001**

Catalog Description

Introduction to computer organization. Octal and Hexadecimal number systems, ASCII codes. Assembly language programming, instruction format and types, memory and I/O instructions, arithmetic instructions, addressing modes, stack operations, and interrupts. ALU and control unit design. RTL, microprogramming, and hardwired control. Practice of assembly language programming.

Prerequisite: COE 200 and ICS 201

Instructor Dr. Aiman H. El-Maleh. Room: 22/332 Phone: 2811 Email: aimane@ccse

Office Hours Sat-Mon-Wed 11:00-12:00, 2:00-3:00 and by appointment

Text Book: *Assembly Language Programming and Computer Architecture*, Joe Carthy, Thomson Computer Press, 1996.

Course Policies	Laboratory	25%	
	Homework & Quizzes	10%	
	Exam I	20%	(tentative date: October 14)
	Exam II	20%	(tentative date: November 13)
	Final	25%	

Course Topics

1. ***Introduction and Information Representation.*** (Appendix B, Notes) **4 lectures**
Introduction. Octal and Hexadecimal number system. Sign-magnitude, 1's complement and 2's complement arithmetic. ASCII code. Computer components.
2. ***Assembly Language Concepts.*** (2.1, 2.3, 2.5, 2.6, 3.3) **6 lectures**
Assembly language format. Directives vs. instructions. Constants and variables. I/O. INT 21H. Addressing modes.
3. ***8086 Assembly Language Prog.*** (3.1, 3.2, 3.4, 3.5, 4.1, 4.3, 4.4, Notes) **15 lectures**
Register set. Memory segmentation. MOV instructions. Arithmetic instructions and flags (ADD, ADC, SUB, SBB, INC, DEC, MUL, IMUL, DIV, IDIV). Compare, Jump and loop (CMP, JMP, Conditional jumps, LOOP). Logic, shift and rotate . Stack operations. Subprograms. Macros. I/O (IN, OUT). String instructions.
4. ***Computer Organization.*** (5.1, Notes) **6 lectures**
Main memory, SRAM, DRAM. External memory, magnetic and optical disks. Bus system. I/O devices. Interrupts and interrupt processing, INT and IRET.

lectures

1-bus, 2-bus and 3-bus CPU organization. Fetch and execute phases of instruction processing. Machine code. Control steps. Hardwired control. Microprogramming.