

COE 202, Term 052
Fundamentals of Computer Engineering
HW# 3

Q.1. For the Boolean function E and F, as given in the following truth table:

X	Y	Z	E	F
0	0	0	1	0
0	0	1	1	0
0	1	0	1	1
0	1	1	0	0
1	0	0	1	0
1	0	1	0	0
1	1	0	0	1
1	1	1	0	1

- (i) List the minterms and the maxterms of each function.
- (ii) List the minterms of E' and F'.
- (iii) List the minterms of E + F and E . F.
- (iv) Express E and F in sum-of-minterms algebraic form.
- (v) Simplify E and F to expressions with a minimum number of literals.

Q.2. Simplify the following Boolean functions **F** together with the don't care conditions **d**. Find all prime implicants and essential prime implicants, and apply the selection rule.

- (i) $F(A, B, C) = \sum m(3, 5, 6)$, $d(A, B, C) = \sum m(0, 7)$
- (ii) $F(A, B, C, D) = \sum m(4, 6, 7, 8, 12, 15)$, $d(A, B, C, D) = \sum m(2, 3, 5, 10, 11, 14)$
- (iii) $F(A, B, C, D) = \sum m(1, 3, 5, 6, 7, 9, 10, 11, 14)$

Q.3. Simplify the following Boolean functions **F** together with the don't care conditions **d** in (1) sum-of-products and (2) product-of-sums form:

- (i) $F(W, X, Y, Z) = \sum m(0, 1, 2, 3, 7, 8, 10)$, $d(W, X, Y, Z) = \sum m(5, 6, 11, 15)$
- (ii) $F(A, B, C, D) = \sum m(3, 4, 13, 15)$, $d(A, B, C, D) = \sum m(1, 2, 5, 6, 8, 10, 12, 14)$
- (iii) $F(A, B, C, D, E, F) = \sum m(6, 9, 13, 18, 19, 25, 27, 29, 41, 45, 57, 61)$

Q.4. The following Boolean expression: $BE + B'DE'$ is a simplified version of the expression: $A'BE + BCDE + BC'D'E + A'B'DE' + B'C'DE'$. Are there any don't care conditions? If so, what are they?

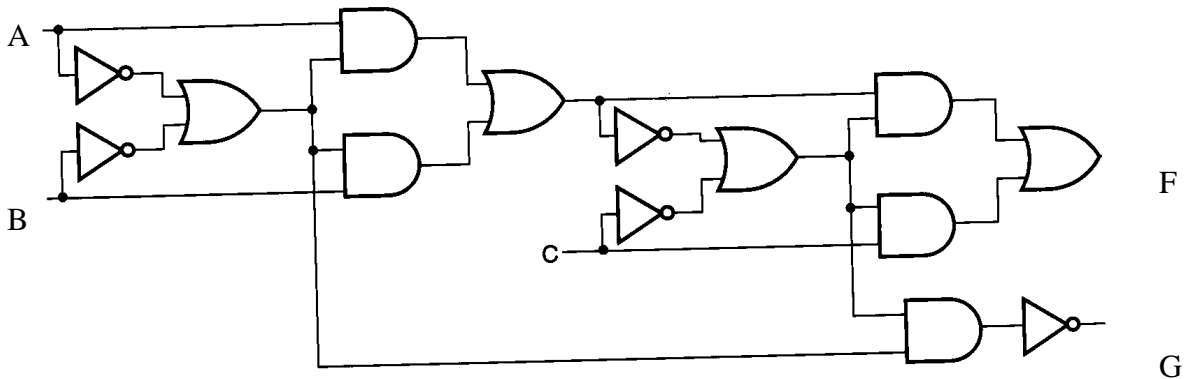
Q.5. Simplify each of the following expressions, and implement them with (1) NAND gates, (2) NOR gates. Assume that both true and complement versions of the input variables are available.

- (i) $WX' + WXZ + W'Y'Z' + W'XY' + WXZ'$
- (ii) $XZ + XYZ' + WX'Y'$

Q.6. Implement the following Boolean function with XOR and AND gates:

$$AB'C'D + A'BC'D + AB'CD' + A'BCD'$$

Q.7. Convert the AND/OR/NOT logic diagram shown below to (a) a NAND logic diagram, and (b) a NOR logic diagram.



Q.8. Derive the exclusive-OR/exclusive-NOR circuits for three-bit parity generator and a four-bit parity checker, using an even parity bit.

Q.9. A NAND gate with seven inputs is required. For each of the following cases, minimize the number of gates used in the multiple-level result:

- (i) Design the 7-input NAND gate using 2-input NAND gates and NOT gates.
- (ii) Design the 7-input NAND gate using 2-input NAND gates, 2-input NOR gates, and NOT gates.

HW#3

Q1 (a) $E = \sum (m_0, m_1, m_2, m_4)$
 $= \prod (M_3, M_5, M_6, M_7)$

$$F = \sum (m_2, m_6, m_7)$$

$$= \prod (M_0, M_1, M_3, M_4, M_5)$$

(b) $\bar{E} = \sum (m_3, m_5, m_6, m_7)$
 $\bar{F} = \sum (m_0, m_1, m_3, m_4, m_5)$

(c) The minterms of $E+F$ is the set of minterms in E union the set of minterms in F

$$E+F = \sum (m_0, m_1, m_2, m_4, m_6, m_7)$$

The minterms of $E \cdot F$ is the set of minterms in E intersected with the set of minterms in F , i.e. the minterms common to both E and F

$$E \cdot F = \sum (m_2)$$

(d) $E = \bar{x}\bar{y}\bar{z} + \bar{x}\bar{y}z + \bar{x}y\bar{z} + x\bar{y}\bar{z}$

$$F = \bar{x}y\bar{z} + x\bar{y}z + xy\bar{z} + xyz$$

(e)

	$\bar{x}\bar{y}$	$\bar{x}y$	$x\bar{y}$	xy
\bar{z}	1	1	0	1
z	1	0	0	0

$$E = \bar{x}\bar{y} + \bar{y}\bar{z} + \bar{x}\bar{z}$$

$$= \bar{y}(\bar{x} + \bar{z}) + \bar{x}\bar{z}$$

5 literals

	$\bar{x}\bar{y}$	$\bar{x}y$	$x\bar{y}$	xy
\bar{z}	0	0	0	1
z	0	0	1	1

$$F = xy + y\bar{z}$$

$$= y(x + \bar{z})$$

3 literals

Q2 (c) $F(A,B,C) = \sum m(3,5,6)$, $d(A,B,C) = \sum m(0,7)$

	BC	00	01	11	10
A	0	X	0	1	0
	1	0	1	X	1

Prime Implicants: BC, AC, AB

All of them are essential

$$F = BC + AC + AB = C(B+A) + AB$$

(ii) $F(A,B,C,D) = \sum m(4,6,7,8,12,15)$

$d(A,B,C,D) = \sum m(2,3,5,10,11,14)$

	CD	00	01	11	10
AB	00	0	0	X	X
	01	X	X	1	1
	11	1	0	X	X
	10	X	0	X	X

Prime implicants: C, $\bar{A}B$, $B\bar{D}$, $A\bar{D}$

Essential prime implicants: C, $A\bar{D}$

After selecting the essential prime implicants, only minterm m4 remains uncovered. This can be covered by selecting the prime implicant $\bar{A}B$ or $B\bar{D}$. Thus,

$$F = C + A\bar{D} + \bar{A}B \quad 5 \text{ literals}$$

$$\text{or } F = C + A\bar{D} + B\bar{D} \quad 5 \text{ literals}$$

$$= C + \bar{D}(A+B) \quad 4 \text{ literals}$$

Note that the 2nd expression is better since it can be factored. This results in a multilevel circuit.

$$(222) \quad F(A, B, C, D) = \prod M(1, 3, 5, 6, 7, 9, 10, 11, 14) \\ = \sum m(0, 2, 4, 8, 12, 13, 15)$$

		CD			
		00	01	11	10
AB	00	1	0	0	1*
	01	*1	0	0	0
	11	1	1	*1	0
	10	1*	0	0	0

Prime implicants: $\bar{C}\bar{D}$, $\bar{A}\bar{B}\bar{D}$, $AB\bar{C}$, ABD

Essential prime implicants: $\bar{C}\bar{D}$, $\bar{A}\bar{B}\bar{D}$, ABD

After selecting the essential prime implicants, all the minterms are covered.

$$F = \bar{C}\bar{D} + \bar{A}\bar{B}\bar{D} + ABD \\ = \bar{D}(\bar{C} + \bar{A}\bar{B}) + ABD$$

Q3

$$(c) \quad F(W, X, Y, Z) = \sum m(0, 1, 2, 3, 7, 8, 10) \\ d(W, X, Y, Z) = \sum m(5, 6, 11, 15)$$

sum of products:

		YZ			
		00	01	11	10
WX	00	1	1	1	1*
	01	0	*1	1	*1
	11	0	0	*1	0
	10	1*	0	*1	1*

Prime implicants: $\bar{W}\bar{X}$, $\bar{X}\bar{Z}$, $\bar{W}Z$, $\bar{W}Y$, YZ , $\bar{X}Y$

Essential prime implicants: $\bar{X}\bar{Z}$

$$F = \bar{X}\bar{Z} + \bar{W}Z$$

Product of sums:

$$F(w, x, y, z) = \sum m(4, 9, 12, 13, 14)$$

$$d(w, x, y, z) = \sum m(5, 6, 11, 15)$$

wx \ yz	00	01	11	10
00	0	0	0	0
01	1	X	0	X
11	1	1	X	1
10	0	X	X	0

Prime implicants: $x\bar{y}$, $x\bar{z}$, wx , wz

Essential prime implicants: wz

$$\bar{F} = wz + x\bar{z}$$

$$F = \bar{\bar{F}} = (\bar{w} + \bar{z})(\bar{x} + z)$$

(ii) $F(A, B, C, D) = \sum m(3, 4, 13, 15)$

$$d(A, B, C, D) = \sum m(1, 2, 5, 6, 8, 10, 12, 14)$$

Sum of products:

AB \ CD	00	01	11	10
00	0	X	1	X
01	1	X	0	X
11	X	1	X	X
10	X	0	0	X

Prime implicants: $\bar{A}\bar{B}D$, $\bar{A}\bar{B}C$, $B\bar{C}$, $B\bar{D}$, AB

$$F = AB + B\bar{C} + \bar{A}\bar{B}D$$

$$\text{or } F = AB + B\bar{C} + \bar{A}\bar{B}C$$

$$\text{or } F = AB + B\bar{D} + \bar{A}\bar{B}D$$

$$\text{or } F = AB + B\bar{D} + \bar{A}\bar{B}C$$

+

Product of Sums:

$$F(A, B, C, D) = \sum m(0, 7, 9, 11)$$

$$d(A, B, C, D) = \sum m(1, 2, 5, 6, 8, 10, 12, 14)$$

AB \ CD	00	01	11	10
00	1	X	0	X
01	0	X	1	X
11	X	0	0	X
10	X	1	X	X

Prime implicants: $\bar{B}\bar{C}$, $\bar{B}\bar{D}$, $A\bar{B}$, $\bar{A}BD$, $\bar{A}BC$

Essential prime implicants: $A\bar{B}$

$$\bar{F} = A\bar{B} + \bar{B}\bar{C} + \bar{A}BD \Rightarrow F = (\bar{A}+B)(B+C)(A+\bar{B}+\bar{D})$$

$$\text{or } \bar{F} = A\bar{B} + \bar{B}\bar{C} + \bar{A}BC \quad \text{or } F = (\bar{A}+B)(B+C)(A+\bar{B}+\bar{C})$$

$$\text{or } \bar{F} = A\bar{B} + \bar{B}\bar{D} + \bar{A}BD \quad \text{or } F = (\bar{A}+B)(B+D)(A+\bar{B}+\bar{D})$$

$$\text{or } \bar{F} = A\bar{B} + \bar{B}\bar{D} + \bar{A}BC \quad \text{or } F = (\bar{A}+B)(B+D)(A+\bar{B}+\bar{C})$$

(iii) $F(A, B, C, D, E, F) = \sum m(6, 7, 13, 18, 19, 25, 27, 29, 41, 45, 57, 61)$

Sum of products:

AB=00

CD \ EF	00	01	11	10
00	0	0	0	0
01	0	0	0	1
11	0	1	0	0
10	0	1	0	0

AB=01

CD \ EF	00	01	11	10
00	0	0	1	1
01	0	0	0	0
11	0	1	0	0
10	0	1	1	0

AB=10

CD \ EF	00	01	11	10
00	0	0	0	0
01	0	0	0	0
11	0	1	0	0
10	0	1	0	0

AB=11

CD \ EF	00	01	11	10
00	0	0	0	0
01	0	0	0	0
11	0	1	0	0
10	0	1	0	0

Prime implicants: $C\bar{E}F$, $\bar{A}\bar{B}\bar{C}DEF$, $\bar{A}B\bar{C}\bar{D}E$, $\bar{A}B\bar{D}EF$,
 $\bar{A}B\bar{C}\bar{D}F$

Essential prime implicants: $C\bar{E}F$, $\bar{A}\bar{B}\bar{C}DEF$, $\bar{A}B\bar{C}\bar{D}E$

$$= C\bar{E}F + \bar{A}\bar{B}\bar{C}DEF + \bar{A}B\bar{C}\bar{D}E + \bar{A}B\bar{C}\bar{D}F$$

$$\text{or } = C\bar{E}F + \bar{A}\bar{B}\bar{C}DEF + \bar{A}B\bar{C}\bar{D}E + \bar{A}B\bar{D}EF$$

Product of sums

$$\bar{F}(A, B, C, D, E, F) = \sum m(0, 1, 2, 3, 4, 5, 7, 8, 10, 11, 12, 14, 15, 16, 17, 20, 21, 22, 23, 24, 26, 28, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 42, 43, 44, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 58, 59, 60, 62, 63)$$

$AB=00$

	EF	00	01	11	10
CD	00	1	1	1	1
	01	1	1	1	0
	11	1	0	1	1
	10	1	0	1	1

$AB=01$

	EF	00	01	11	10
CD	00	1	1	0	0
	01	1	1	1	1
	11	1	0	1	1
	10	1	0	0	1

$AB=10$

	EF	00	01	11	10
CD	00	1	1	1	1
	01	1	1	1	1
	11	1	0	1	1
	10	1	0	1	1

$AB=11$

	EF	00	01	11	10
CD	00	1	1	1	1
	01	1	1	1	1
	11	1	0	1	1
	10	1	0	1	1

Prime implicants:

$\bar{C}\bar{E}$, $\bar{E}\bar{F}$, $\bar{B}\bar{E}\bar{D}$, $\bar{B}\bar{E}F$, $\bar{B}\bar{C}F$,
 $\bar{B}\bar{C}E$, $C\bar{F}$, CDE , $A\bar{C}$, AE ,
 $A\bar{C}D$, $B\bar{C}D$, $A\bar{C}F$, BDE , DEF ,
 $\bar{B}\bar{D}E$

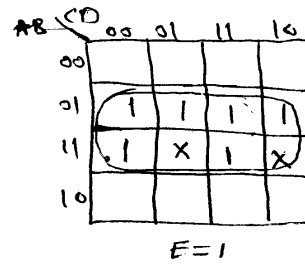
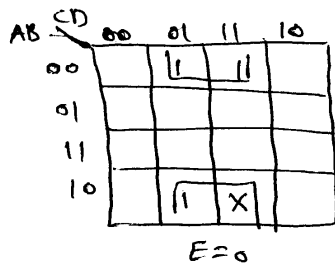
Essential prime implicants: $\bar{C}\bar{E}$, AE

$$F = \bar{C}\bar{E} + AE + C\bar{F} + \bar{B}EF + BDE + \bar{B}\bar{D}\bar{E}$$

This is one possible minimal expression. There are also other possibilities.

$$F = \bar{F} = (C+E)(\bar{A}+\bar{E})(\bar{C}+F)(B+\bar{E}+\bar{F})(\bar{B}+\bar{D}+\bar{E})(B+\bar{D}+\bar{E})$$

Q4



The don't care conditions are:

$$d(A, B, C, D, E) = \sum m(22, 27, 29)$$

Q5

$$(E) \quad w\bar{x} + wxz + \bar{w}\bar{y}\bar{z} + \bar{w}x\bar{y} + wx\bar{z}$$

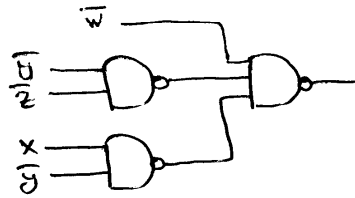


$$F = w + \bar{y}\bar{z} + x\bar{y}$$

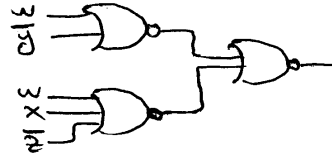
$$\bar{F} = \bar{w}y + \bar{w}\bar{x}z$$

$$F = \bar{F} = (w + \bar{y})(w + x + \bar{z})$$

Nand Implementation



Nor Implementation



(11) $xz + xy + w\bar{x}\bar{y}$

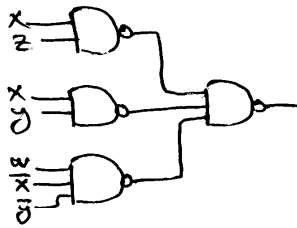
wx\yz	00	01	11	10
00	0	0	0	0
01	0	1	1	1
11	0	1	1	1
10	1	1	0	0

$F = xz + xy + w\bar{x}\bar{y}$

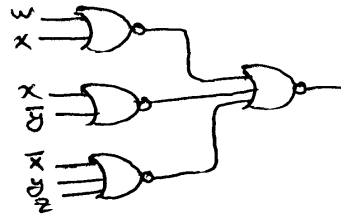
$\bar{F} = \bar{w}\bar{x} + \bar{x}\bar{y} + x\bar{y}\bar{z}$

$F = \bar{F} = (w+x)(x+\bar{y})(\bar{x}+y+z)$

Nand Implementation



Nor Implementation



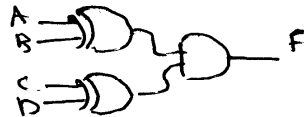
Q6

$$F = A\bar{B}\bar{C}D + \bar{A}B\bar{C}D + A\bar{B}C\bar{D} + \bar{A}B C\bar{D}$$

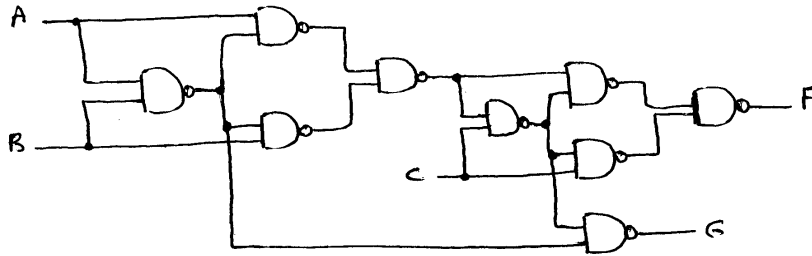
$$= A\bar{B}(\bar{C}D + C\bar{D}) + \bar{A}B(\bar{C}D + C\bar{D})$$

$$= (\bar{C}D + C\bar{D})(A\bar{B} + \bar{A}B)$$

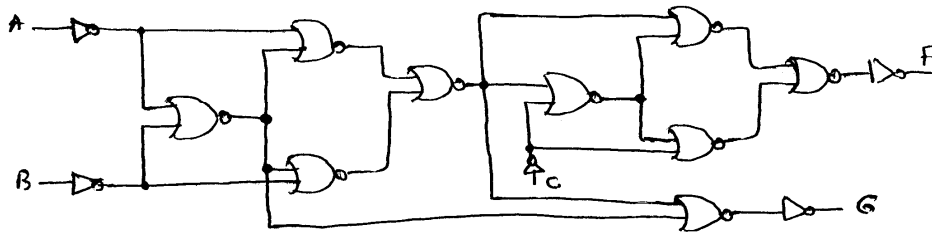
$$= (C \oplus D)(A \oplus B)$$



Q7 a. Nand implementation

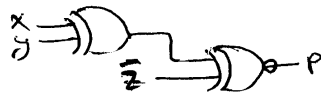


b. NOR implementation

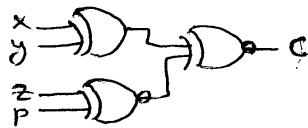


Q8

3-bit parity generator with even parity:

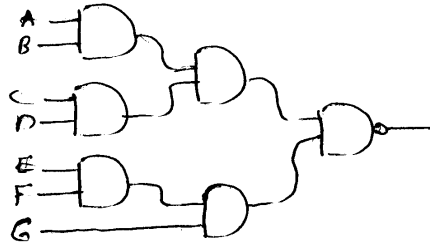


4-bit parity checker with even parity:

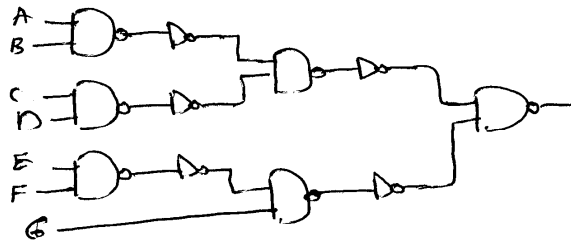


Note that an error occurs if $c = 1$.

Q9 A 7-input NAND gate can be implemented as follows:



a. Using 2-input NAND gates and NOT gates



b. Using 2-input NAND gates, 2-input NOR gates and NOT gates

