

Course Assessment Summary
COE 203 Digital Logic Laboratory (0-3-1)
Term 062

Course Learning Outcomes

1. The ability to design combinational and sequential circuits to meet certain specifications.
2. The ability to use tools and discrete components, EEPROMs, FPGAs, to model, simulate and implement digital circuits.
3. The ability to design and conduct experiments related to digital systems and to analyze their outcomes.
4. The ability to work in teams.
5. The ability to communicate effectively.

Section#	Source of Outcome Data	Outcome1	Outcome2	Outcome3	Outcome4	Outcome5
51 & 52	Instructor Evaluation	82%	82%	70%	86%	78%
	Student Survey	80%	83%	76%	92%	84%
53 & 56	Instructor Evaluation	80.5%	81%	81.3%	88%	82%
	Student Survey	80%	83%	76%	92%	84%
54 & 55	Instructor Evaluation	80.3%	81.7%	80%	88%	82%
	Student Survey	80%	83%	76%	92%	84%
Overall	Assessment Rating	Achieved	Achieved	Needs Improvement	Achieved	Achieved

Observations:

1. Based on both direct and indirect assessments, all outcomes are considered achieved. However, outcome 3 needs further improvements.
2. Students' comments indicate that the course is 1 credit hour but it is more demanding and increasing its weight should be considered.

Recommendations:

1. More emphasis needs to be given to some outcomes like outcome 4 and 5 to better prepare the students.
2. Some of the experiments need to be directed to focus on the achievement of outcome 3.
3. It is recommended by one of the instructors that the lab should be focused around several projects by grouping sets of experiments into projects.